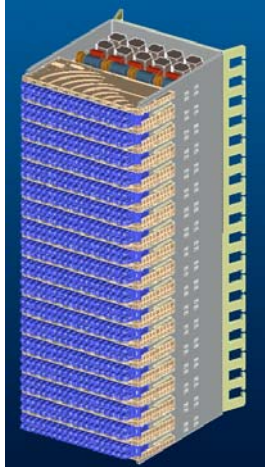


Data sheet

xDSL Block S1000RT with ADSL-over-POTS Splitter according ANSI



Real product may slightly differ from figure

Order number for 72 ports per block: S30264-D1013-V182

Technical Data:

1. Height: 255,3 mm
2. Width of jumper comb: 105 mm
3. Width of wire guides: 103 mm
4. Total depth: 133 mm
5. Depth of the chassis: 116 mm
6. Weight: N/A
7. Number of lines: 72
8. Number of splitter boards: 18
9. Number of lines per board: 4
10. Number of mounting bolts: 2
11. Distance between mounting bolts: 210 mm
12. Environmental operating condition (non-condensing): acc. ETSI EN 300 019-1-3 Class 3.2
13. Environmental transportation condition: acc. ETSI EN 300 019-1-2 Class 2.3
14. Environmental storage condition: acc. ETSI EN 300 019-1-1 Class 1.2
15. Flame retardant: acc. UL 94 V-0
16. PCB base material: FR4 (green material)
17. PCB thickness: 1,7 mm
18. Copper laminate: $\geq 35 \mu\text{m}$
19. Connector pins: CuZn 30 / 3-5 μm Sn over 1-2 μm Ni
20. EMC compliance: acc. ETSI EN 300 386
21. Resistibility: acc. ITU-T K.45 basic test level
22. Splitter performance: acc. ANSI T1.413-1998 Annex E
23. DC-blocking: 120 nF on splitter PCB
24. Maintenance test signature: not provided – optional
25. Reliability (MTBF): ≥ 111 years
26. Tip-to-ring operating voltage (DC): 0 to -60 V
27. Max. voltage (AC): 250 V for 15 minutes
28. Operating current (DC): ≤ 100 mA
29. Overcurrent protection: resettable fuses 180 mA (PPTC device)
30. Overvoltage protection: 2-electrode secondary protec. Device

For splitter performance details refer to page three.

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			Bearb.	R. Vogt		
			Gepr.			
			Norm			
			EP			
			CORNING			Blatt
			S30264-D1013-V182-*-76B1			1+
01		24.11.2005	RV			
Zust.	Mitteilung	Datum	Name		Design Control Location: Hagen/ Germany	

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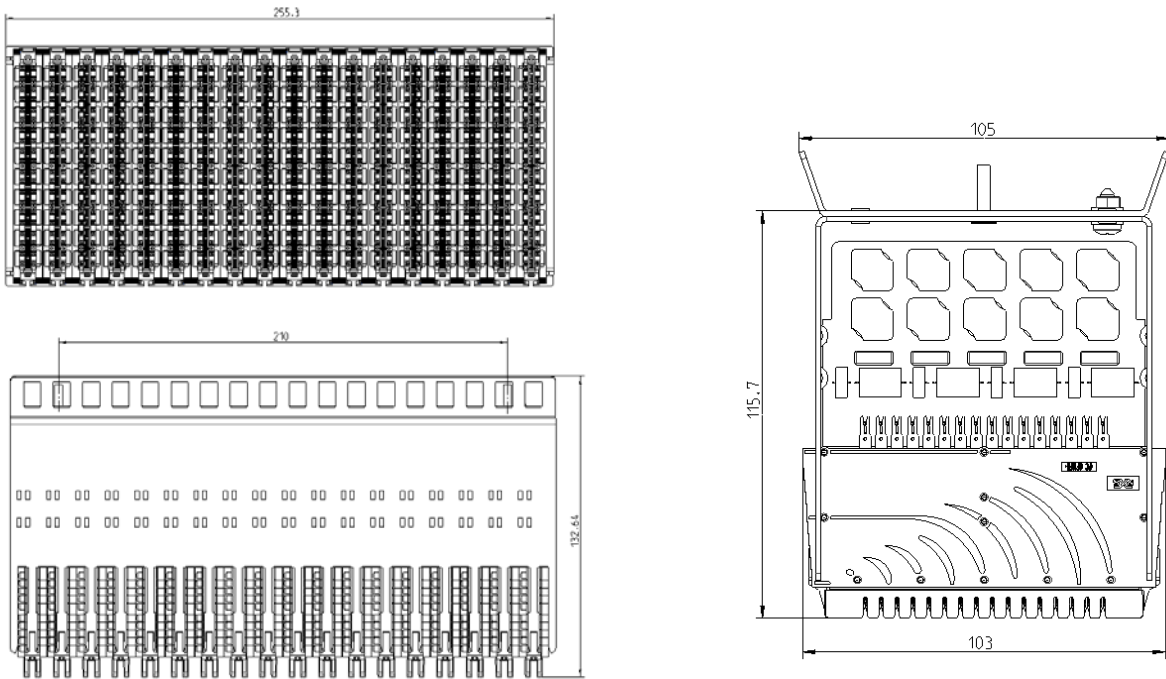
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Technical Details IDC Element:

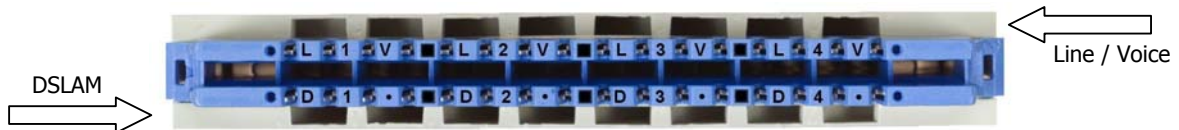
1. Contact principle: acc. IEC 352
2. Contact force: ≥ 9 N (0,32 mm conductor diameter)
3. Contact surface: Ag (2 – 5 μ m)
4. Suitable for: solid conductor 0,32 – 0,8 mm
5. Service life: ≥ 200 clampings (0,8 mm ≥ 50)
6. Volume resistance at clamping point: ≤ 3 m Ω
7. Resistance change: ≤ 5 m Ω
8. Dielectric strength: 1500 V AC (without splitter)
9. Current carrying capacity: $\geq 2,5$ A (without splitter)
10. Insulation resistance: $\geq 10^{12}$ Ω (without splitter)
11. Coupling capacitance: ≤ 5 pF
12. Surge voltage: ≥ 1500 V
13. Noise voltage during vibration: $\leq 0,4$ mV
14. Material of module body: Polycarbonate
15. Utilization category acc. IEC 68-1: 25/100/21

Dimensions in mm:



Contact Scheme of IDC Element:

The signal allocation of the functional element is as shown in the picture below. Each subscriber is marked with a number 1 to 4 and provides certain ports. The wire guides of the element are intended for vertical installation of the MDF splitter block.



- L = Line port (narrowband and broadband service) – connection of the line to the customer
- V = Voice or POTS port (narrowband) – connection of the voice band service from the switch
- D = DSLAM or Data port (broadband) – connection of the data service from the DSLAM

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		Bearb. R. Vogt			
		Gepr.			
		Norm			
		EP			
		CORNING		S30264-D1013-V182-*-76B1	Blatt 2+
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POTS-Splitter Parameter:

The table shows the limits with following termination impedances:

- $Z_{TC} = 900 \Omega$
- $Z_{TR} = 600 \Omega$
- $Z_{LINE} = 100 \Omega$
- $Z_{HP-c} = 100 \text{ nF} + (0,47 \text{ mH} \parallel 100 \Omega) + 100 \text{ nF}$
- $Z_{NL-c} = 800 \Omega \parallel (100 \Omega + 50 \text{ nF})$

The Z_{NL-c} value comes from the Bellcore LSSGR as a reference compromise impedance for nonloaded cables.

The insertion loss, attenuation distortion, delay distortion and return loss requirements are valid with the following specified test loops:

- short loops: 0; 0,5 kft; 2 kft; 5 kft pairs of 26 AWG cables
- long loops: ANSI T1.601 resistance design loops 7, 9, 13 and T1 TR28 CSA loops 4, 6, 7, 8

Parameter	Value	Frequency	Condition
Passband		200 Hz – 4 kHz	
Stopband		30 kHz – 1104 kHz	
DC feeding current	$\leq 100 \text{ mA}$		
DC loop voltage	0 to -60 V		tip-to-ring
Ringing signals	103 V_{RMS}	20 Hz – 30 Hz	superimposed
DC resistance between a or b to earth	$\geq 100 \text{ M}\Omega$		@ 100 VDC
DC resistance between a and b	$\geq 5 \text{ M}\Omega$		@ 100 VDC
DC series resistance	$\leq 25 \Omega$		tip and ring shorted
Insertion loss	$\leq 1 \text{ dB}$ (short loops) $\leq 0,75 \text{ dB}$ (long loops)	@ 1004 Hz	$Z_{TC}, Z_{Tr}, (Z_{HP-c})$
Attenuation distortion (increase relative to insertion loss @ 1004 Hz)	+ 1,5 dB to -1,5 dB (short loops) + 2 dB to -2 dB (short loops) + 0,5 dB to -1,5 dB (long loops) + 1 dB to -1,5 dB (long loops)	200 Hz – 3,4 kHz 3,4 kHz – 4 kHz 200 Hz – 3,4 kHz 3,4 kHz – 4 kHz	$Z_{TC}, Z_{Tr}, (Z_{HP-c})$
Delay distortion	$\leq 200 \mu\text{s}$ (short & long loops) $\leq 250 \mu\text{s}$ (short & long loops)	600 Hz – 3,2 kHz 3,2 kHz – 4 kHz	$Z_{TC}, Z_{Tr}, (Z_{HP-c})$
Return loss (for all test loops)	> 8 dB ERL, > 5 dB SRL-L, > 5 dB SRL-H > 2 dB SRL-H	200 Hz – 4 kHz single frequency	$Z_{NL-c}, Z_{TC}, (Z_{HP-c})$
Intermodulation distortion	$\geq 57 \text{ dB}$ $\geq 60 \text{ dB}$	2 nd order 3 rd order	Z_{TC}, Z_{Tr}
Longitudinal balance	$\geq 58 \text{ dB}$ $\geq 58 \text{ dB to } 53 \text{ dB}$ linearly decreasing	200 Hz – 1 kHz 1 kHz – 3 kHz	25 mA DC bias DSL port shorted
Tip-to-ring capacitance, POTS port	$20 \text{ nF} \leq C \leq 90 \text{ nF}$	20 Hz – 30 Hz	
Capacitance to ground, POTS port	$\leq 1 \text{ nF}$	20 Hz – 30 Hz	
ADSL band attenuation	$\geq 65 \text{ dB}$ $\geq 55 \text{ dB}$	30 kHz – 300 kHz 300 kHz – 1104 kHz	$Z_{TC}, Z_{LINE}, Z_{HP-c}$
Input impedance	$\leq 0,25 \text{ dB}$	30 kHz – 1104 kHz	$Z_{TC}, Z_{LINE}, (Z_{HP-c})$

- ERL = echo return loss
- SRL-L = singing return loss low
- SRL-H = singing return loss high



The product may incorporate materials, including electrical and/ or electronic components that may not be disposed of as household waste.

Disposal must be carried out in strict compliance with the local laws currently in force.

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